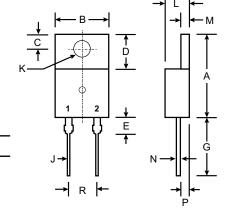


MBR830 - MBR860

8.0A SCHOTTKY BARRIER RECTIFIER

Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Application
- Plastic Material: UL Flammability Classification Rating 94V-0



Pin 2 **0**-

| TO-220AC | | | | | | | |
|----------------------|-------|-------|--|--|--|--|--|
| Dim | Min | Max | | | | | |
| Α | 14.22 | 15.88 | | | | | |
| В | 9.65 | 10.67 | | | | | |
| С | 2.54 | 3.43 | | | | | |
| D | 5.84 | 6.86 | | | | | |
| E | _ | 6.35 | | | | | |
| G | 12.70 | 14.73 | | | | | |
| J | 0.51 | 1.14 | | | | | |
| K | 3.53Ø | 4.09∅ | | | | | |
| L | 3.56 | 4.83 | | | | | |
| М | 1.14 | 1.40 | | | | | |
| N | 0.30 | 0.64 | | | | | |
| P | 2.03 | 2.92 | | | | | |
| R | 4.83 | 5.33 | | | | | |
| All Dimensions in mm | | | | | | | |

Mechanical Data

Case: Molded Plastic

Terminals: Plated Leads Solderable per

MIL-STD-202, Method 208

Polarity: See Diagram

Weight: 2.24 grams (approx.)

Mounting Position: Any

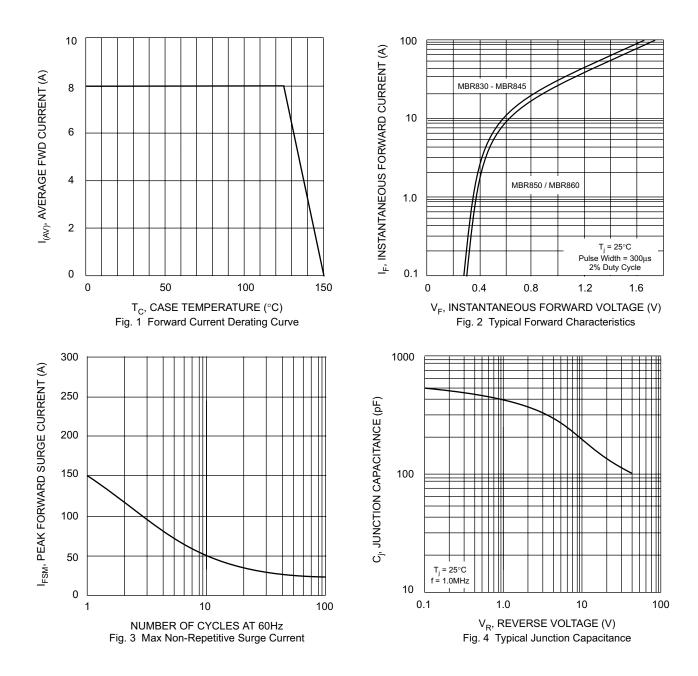
Marking: Type Number

Maximum Ratings and Electrical Characteristics @ TA = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| Characteristic | s | Symbol | MBR 830 | MBR 835 | MBR 840 | MBR 845 | MBR 850 | MBR 860 | Unit |
|-----------------------------------------------------------------------------------------------------------------------|------|--------------------------------------------------------|-------------|-------------------|------------|------------|------------|----------------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | | V _{RRM} V _{RWM} V _R | 30 | 35 | 40 | 45 | 50 | 60 | V |
| RMS Reverse Voltage | | / _{R(RMS)} | 21 | 24.5 | 28 | 31.5 | 35 | 42 | V |
| Average Rectified Output Current (Note 1) @ T _C = 125°C | | Io | 8.0 | | | | | | Α |
| Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method) | | I _{FSM} | 150 | | | | | | А |
| Repetitive Peak Reverse Surge Current @ $t \le 2.0 \mu s$ | | I _{RRM} | 1.0 | | | | | | Α |
| Forward Voltage Drop | 25°C | V _{FM} | | 0.0 0.1 0.1 | 70 | | 0. | 70 80 95 | V |
| $ \begin{array}{cccccccccccccccccccccccccccccccccccc$ | | I _{RM} | 0.1 15 | | | | | | mA |
| Typical Junction Capacitance (Note 2) | | Cj | 250 | | | | | | pF |
| Typical Thermal Resistance Junction to Case (Note 1) | | R _θ JC | 3.0 | | | | | | K/W |
| Voltage Rate of Change (Rated V _R) | | dV/dt | 1000 | | | | | | V/μs |
| Operating and Storage Temperature Range | | Γ _{j,} T _{STG} | -65 to +150 | | | | | | °C |

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